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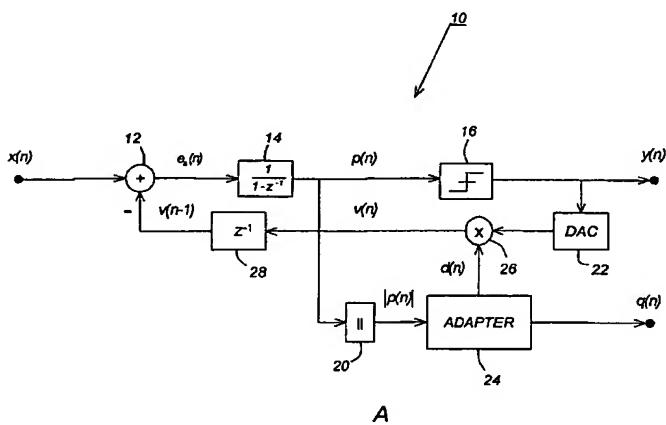
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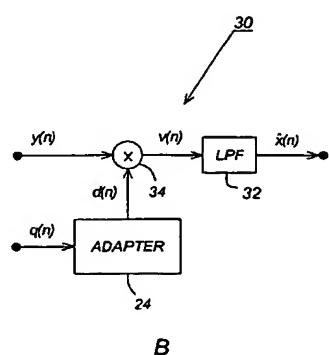
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[Continued on next page]

**(54) Title: ADAPTIVE MULTI-BIT DELTA AND SIGMA-DELTA MODULATION**



**(57) Abstract:** An adaptive multi-bit delta and sigma-delta modulation (10) and demodulation technique, wherein a one-bit modulator generates a binary output signal from an analog input signal and a multi-bit adapter (24) for generating a scaling signal for scaling a step-size of the sigma-delta modulator.





MN, MW, MX, MZ, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

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## A. CLASSIFICATION OF SUBJECT MATTER

IPC(7) : H03M 03/00, 07/30, 07/34; H04B 14/06  
US CL : 341/51, 76, 143; 375/244, 247

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched  
NONE

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)  
NONE

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 6,339,621 B1 (COJOCARU ET AL) 15 JANUARY 2002 (15/01/02), SEE THE ABSTRACT.	1-14

Further documents are listed in the continuation of Box C.

See patent family annex.

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document member of the same patent family

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